

FIG. 1  
(PRIOR ART)

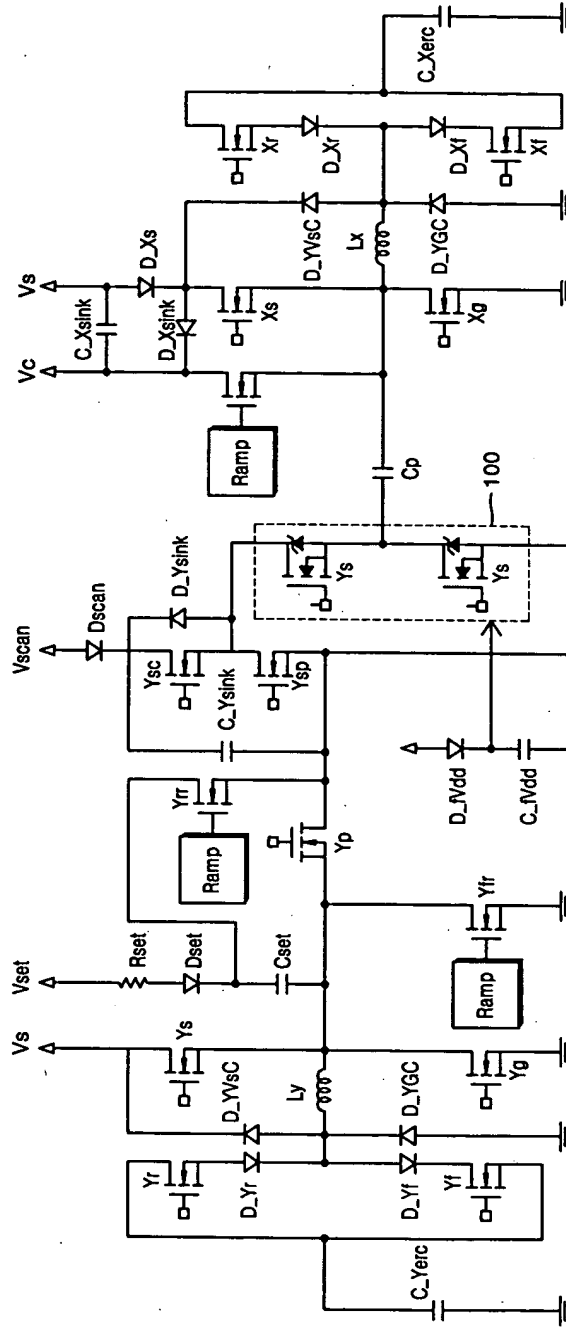


FIG. 2  
(PRIOR ART)

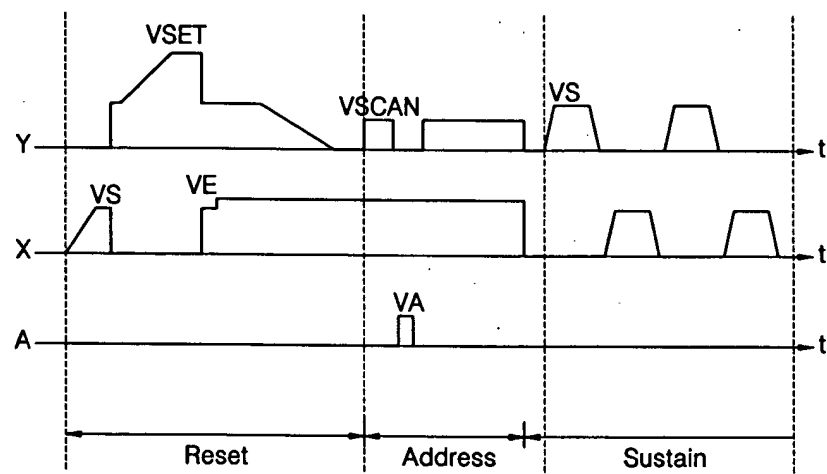


FIG. 3

The diagram illustrates a Scan Driver IC circuit. It includes a VSS supply, a +3/2Vs supply, a +Vs supply, and a +1/2Vs supply. The circuit features several MOSFETs (Yp, Ysc, Ysp, Ytr, Yr, Yl, Xr, Xl, Xe) and diodes (D1, D2, D3, D4, D5, D6, Dx). Capacitors Cx1, Cx2, Cy1, Cy2, Csc, and Cstg are used for timing and signal coupling. A dashed box labeled 'Scan Driver IC' encloses the central MOSFET and diode network. The output is connected to a load capacitor Cp and a signal line X.

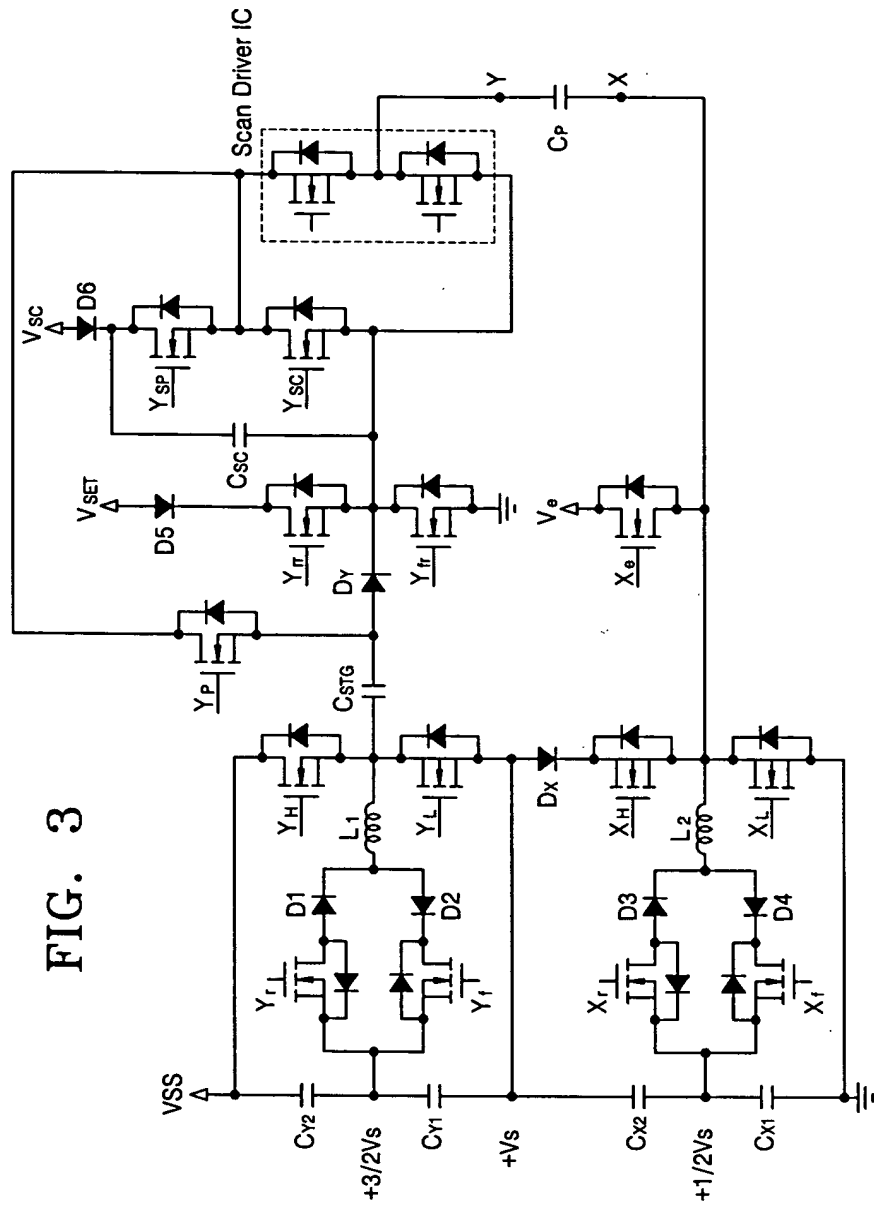


FIG. 4

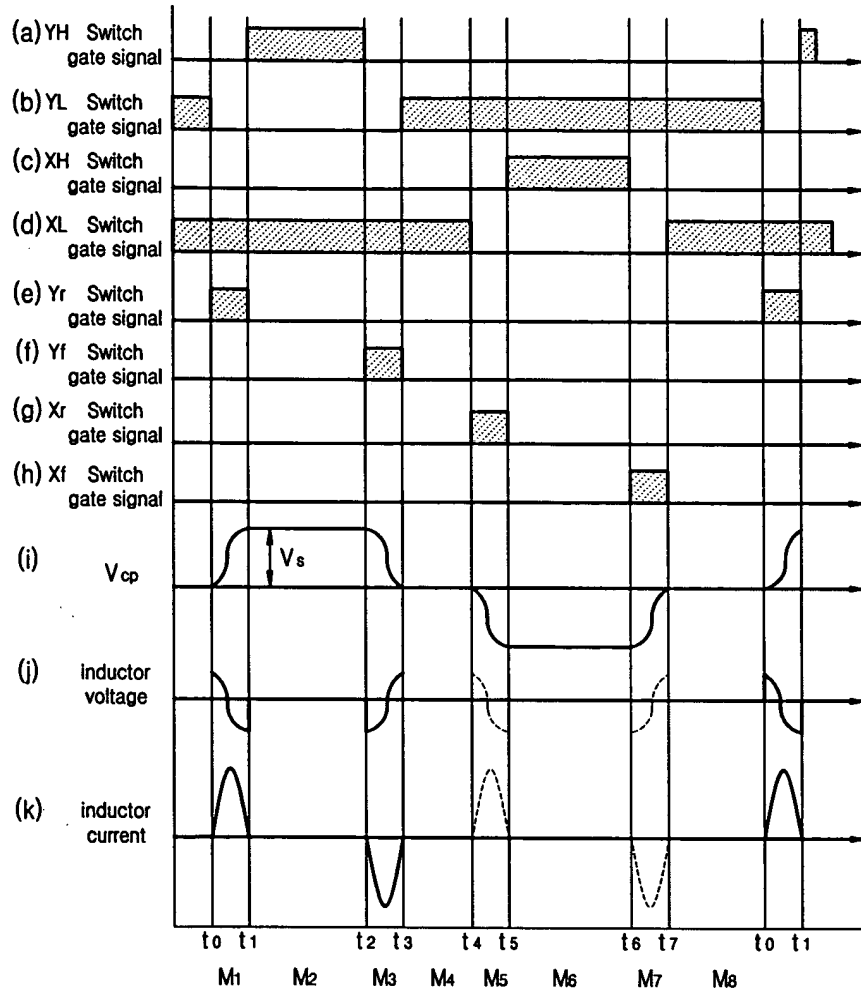


FIG. 5A

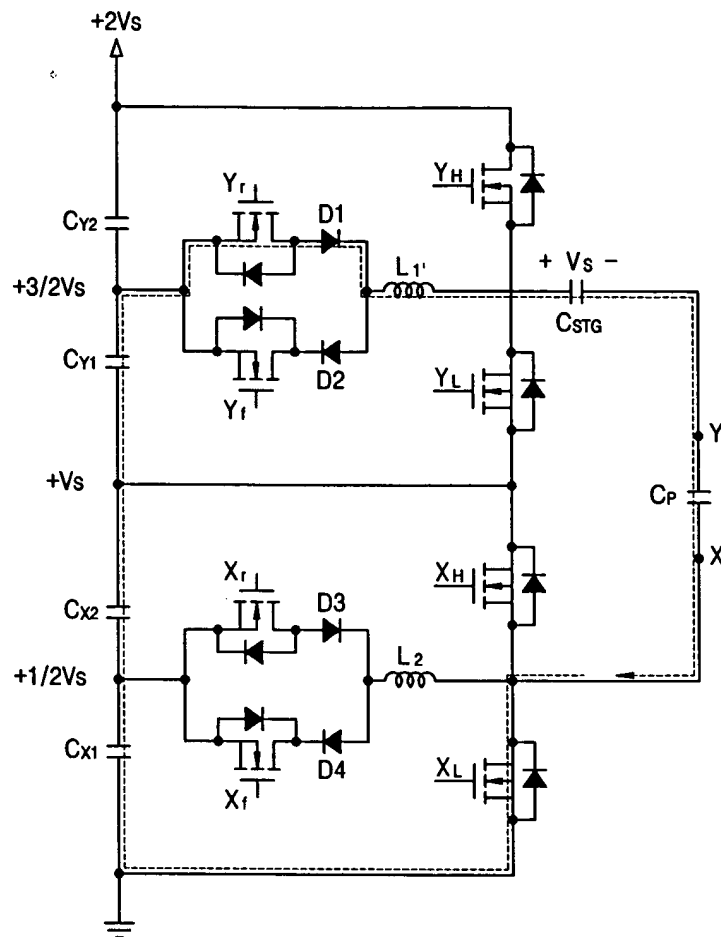


FIG. 5B

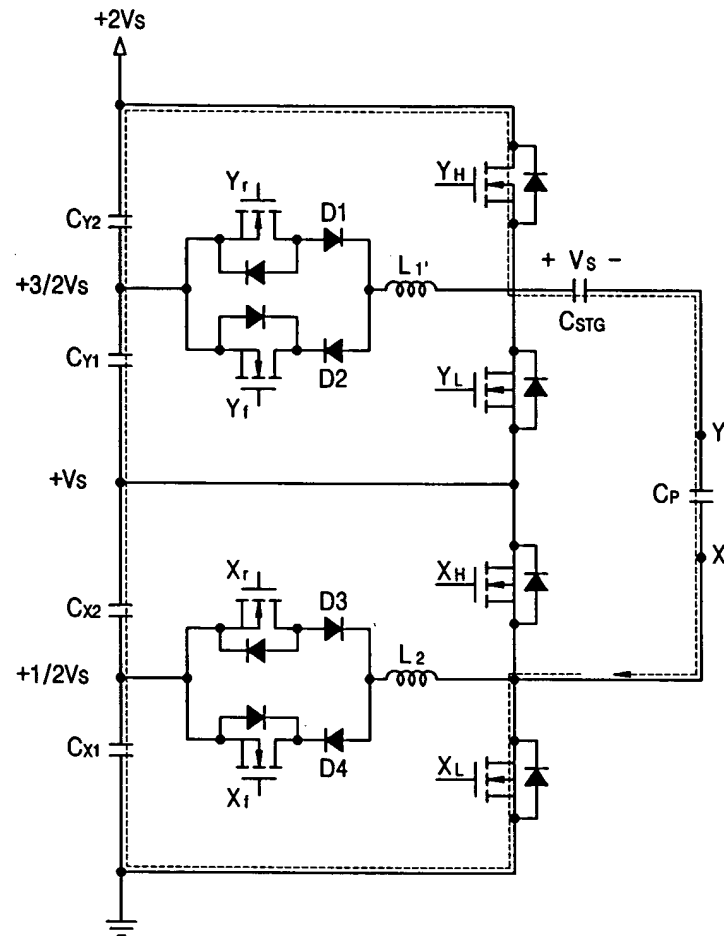


FIG. 5C

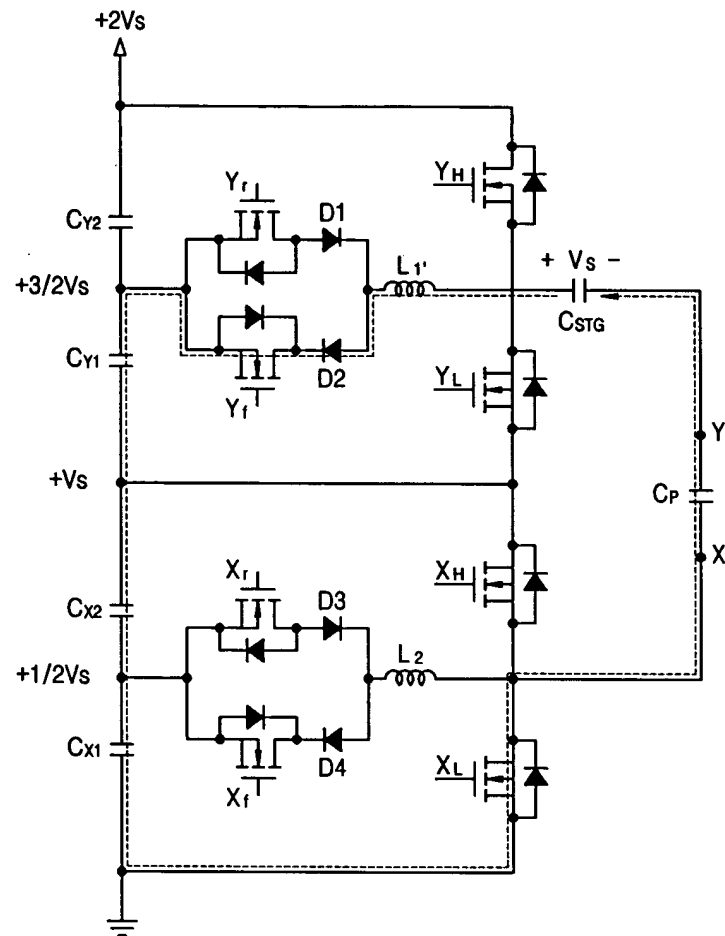


FIG. 5D

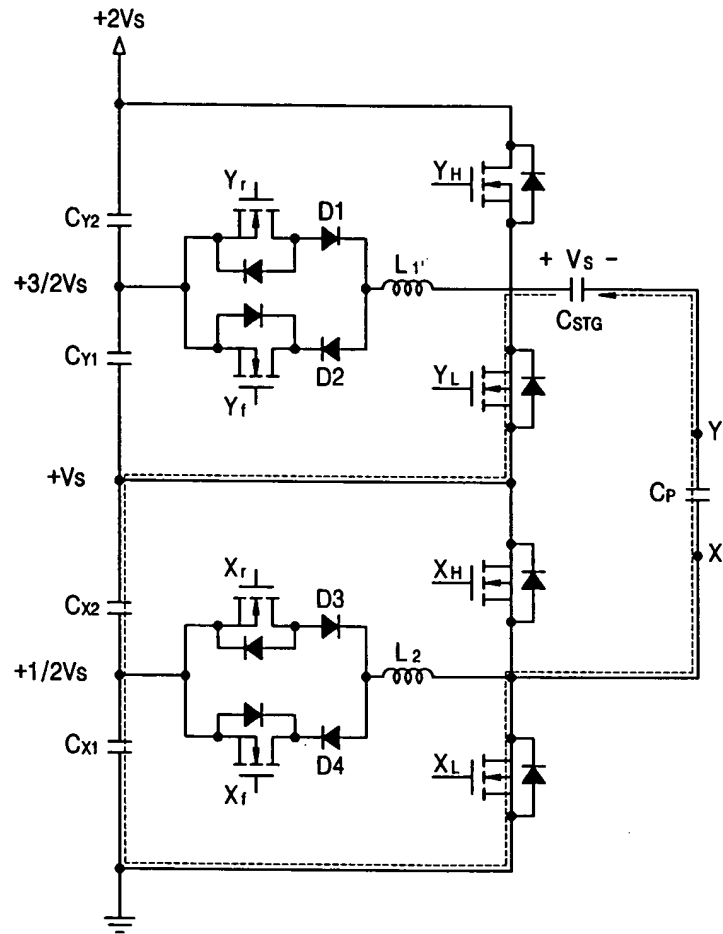




FIG. 5E

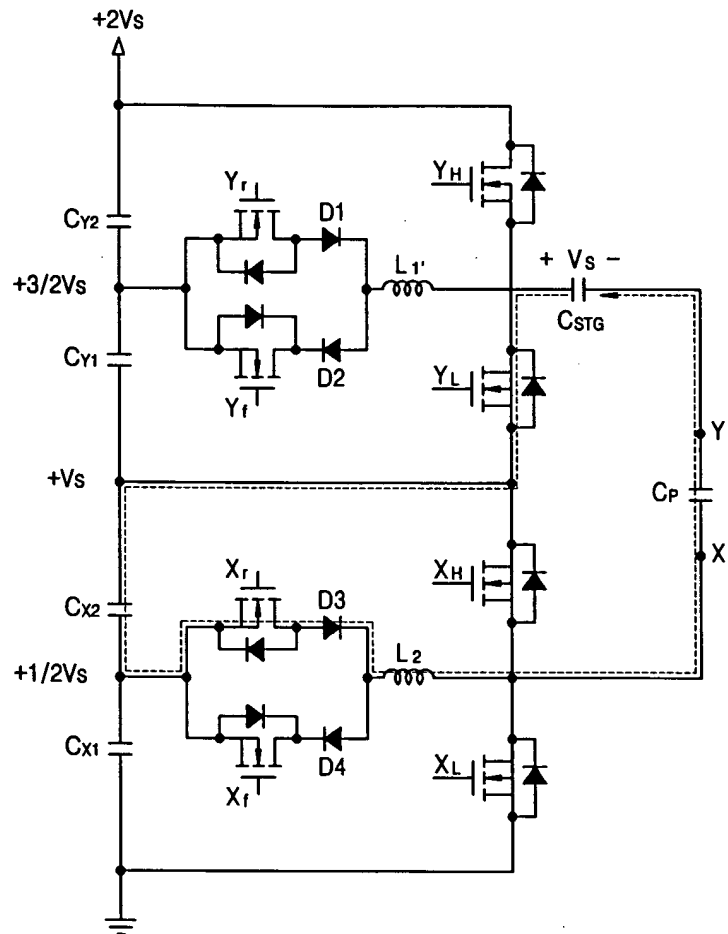


FIG. 5F

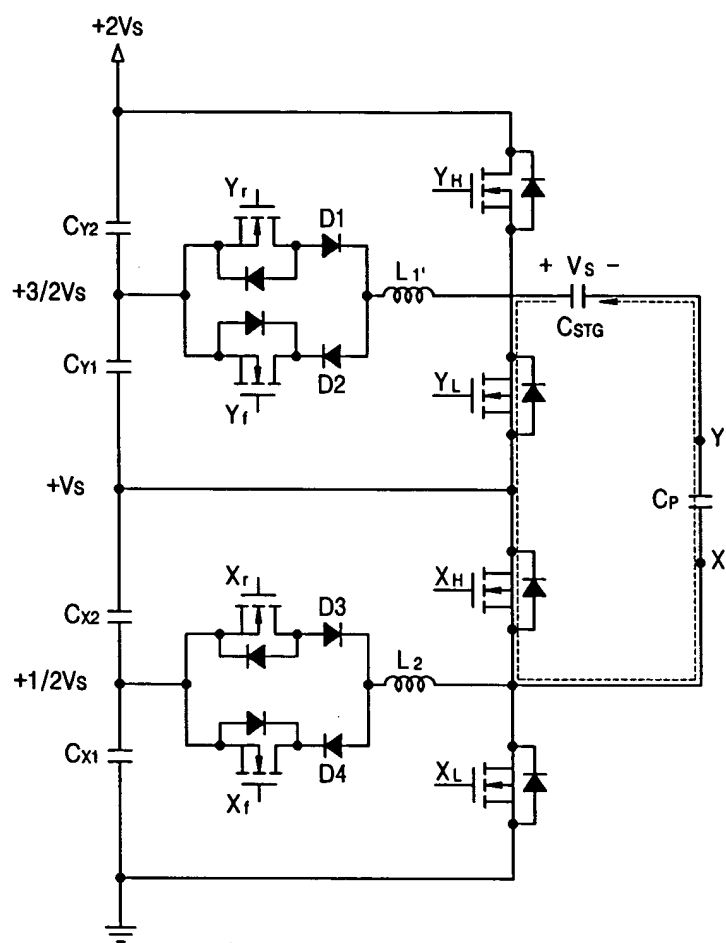


FIG. 5G

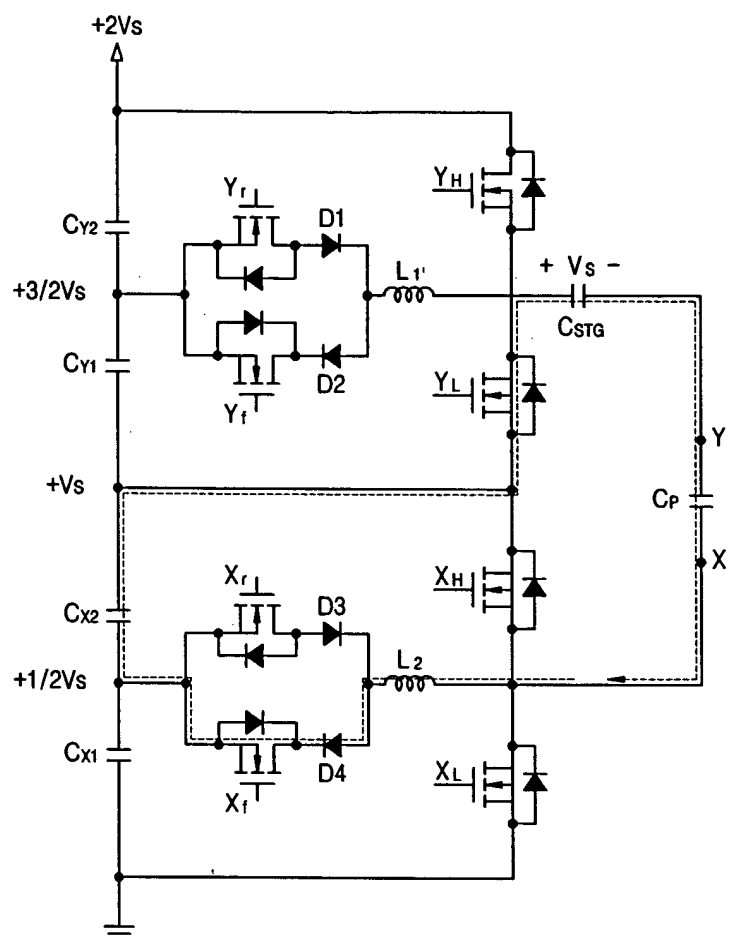


FIG. 5H

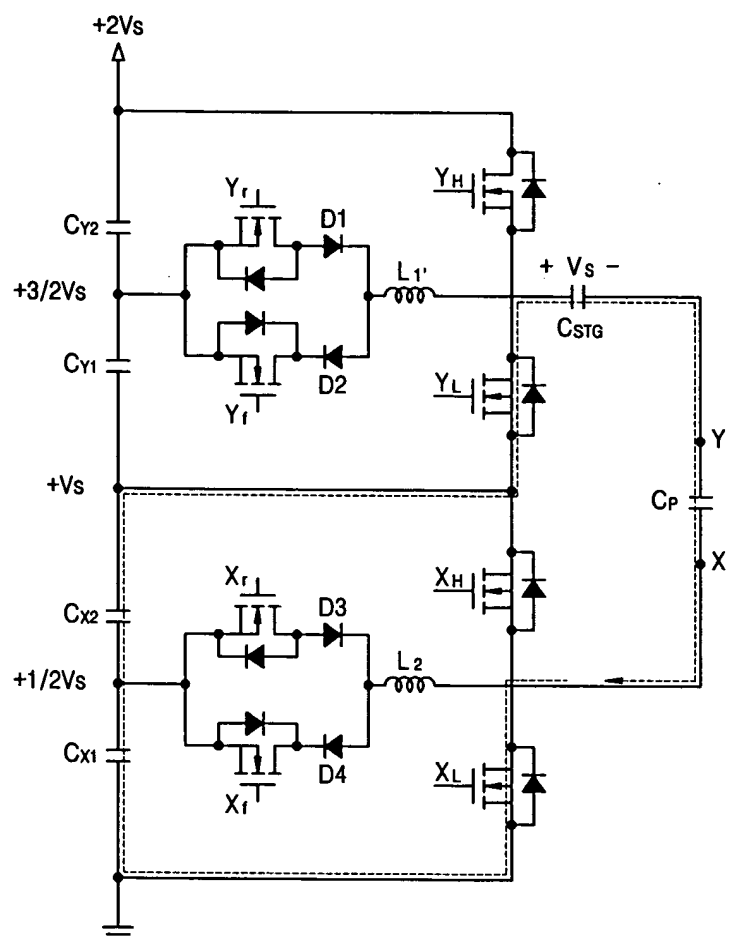


FIG. 6

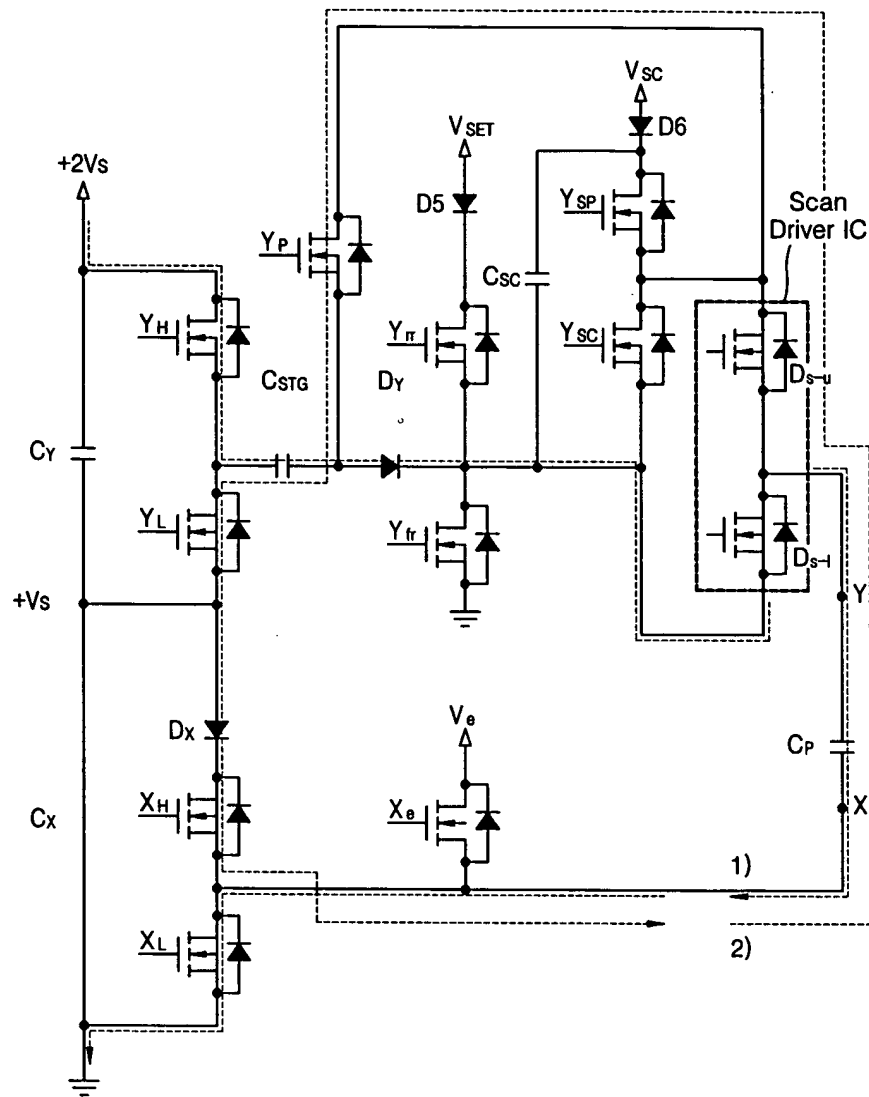


FIG. 7A

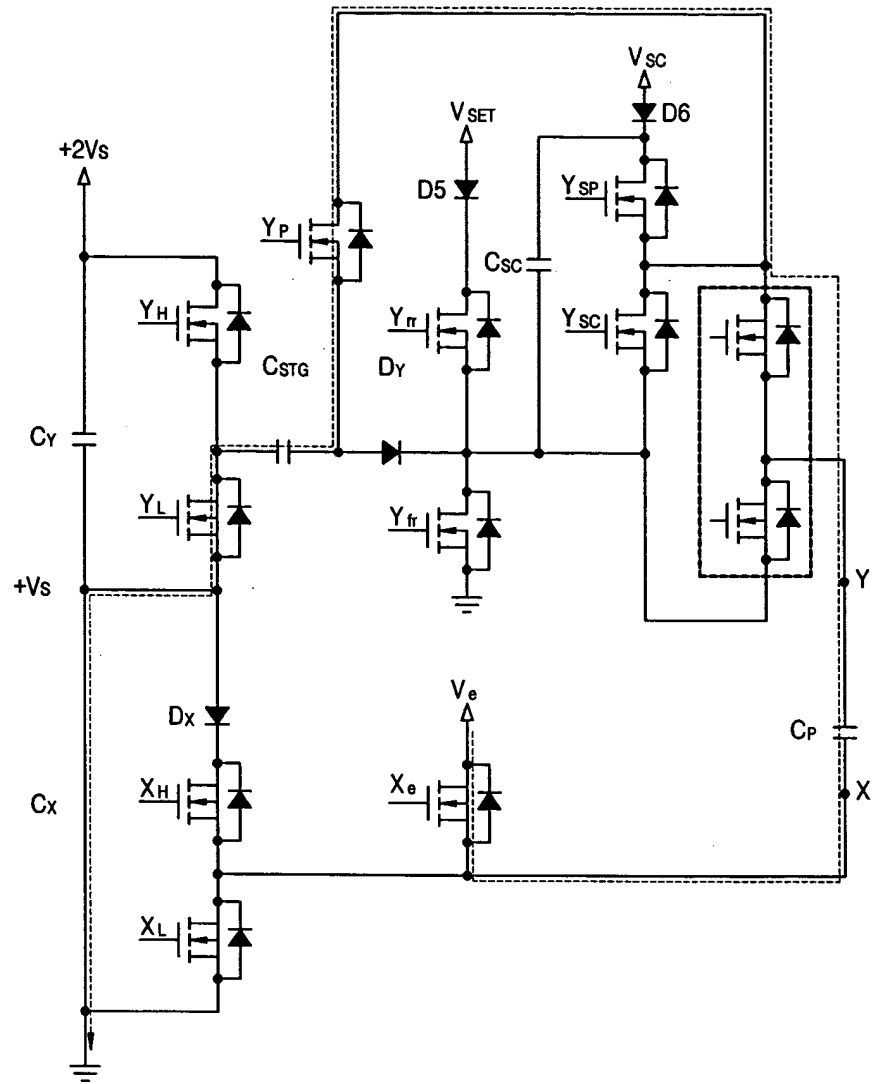


FIG. 7B

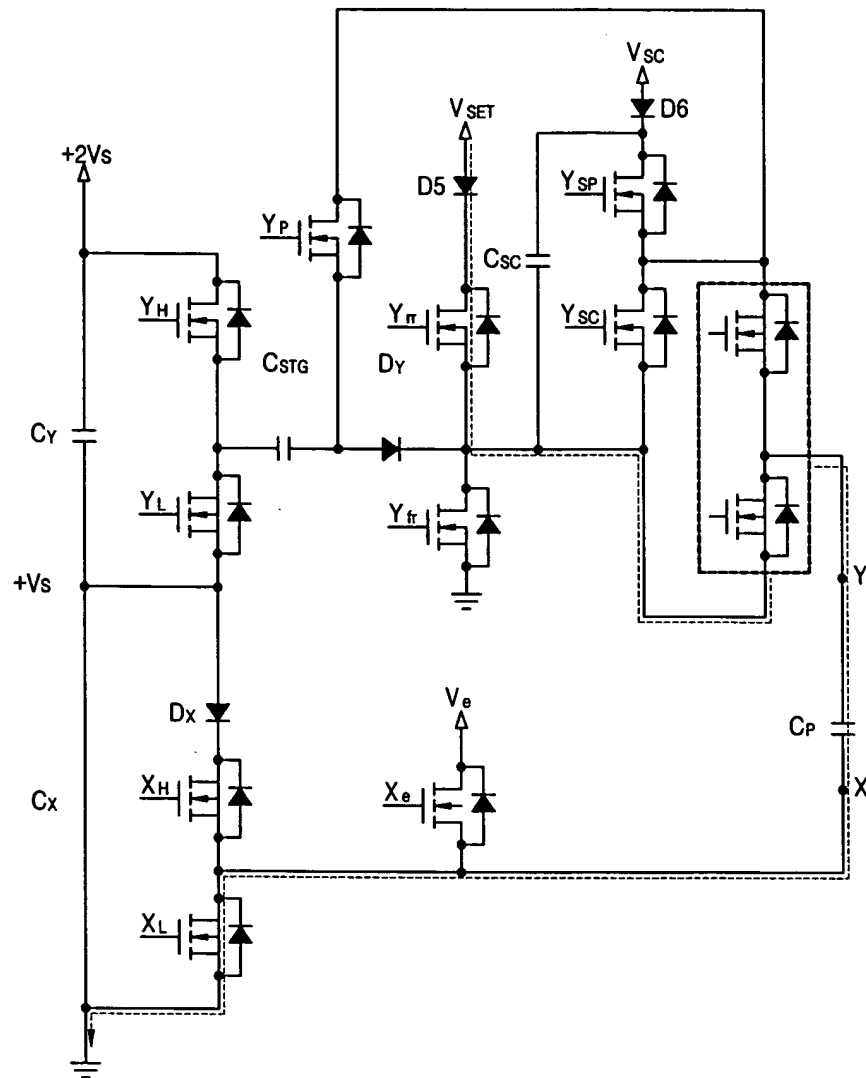


FIG. 7C

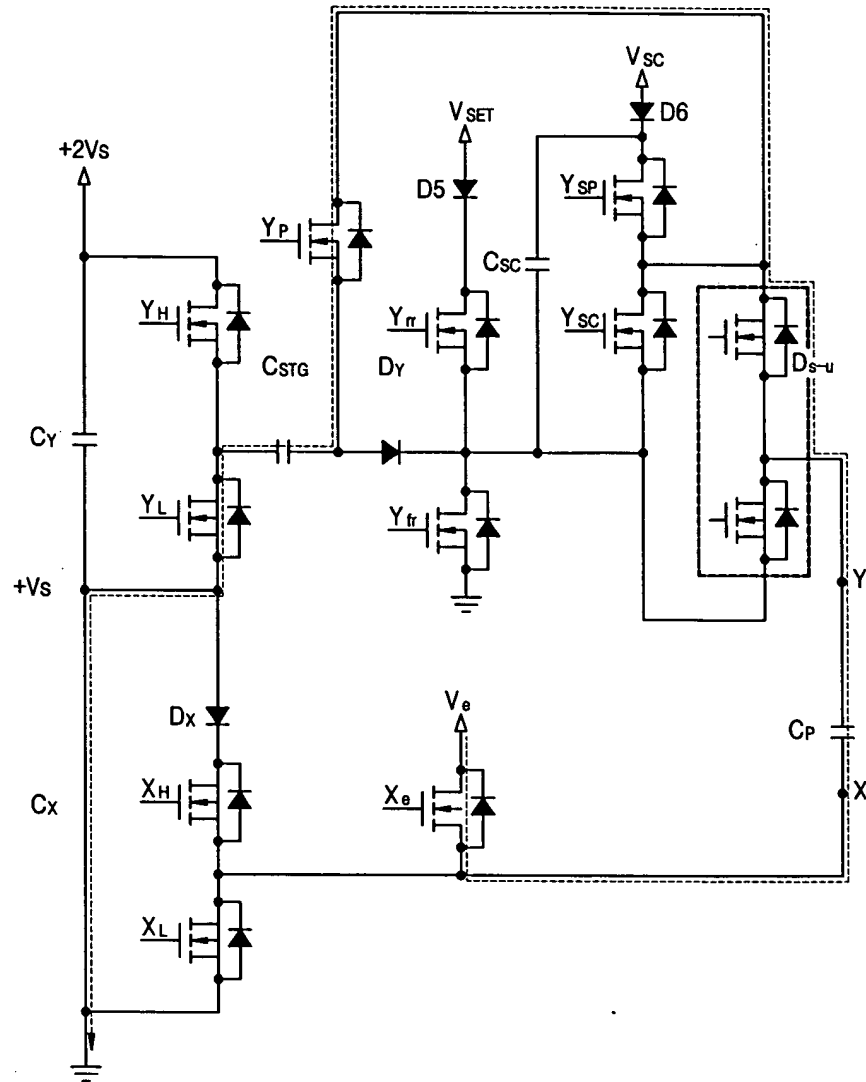




FIG. 7D

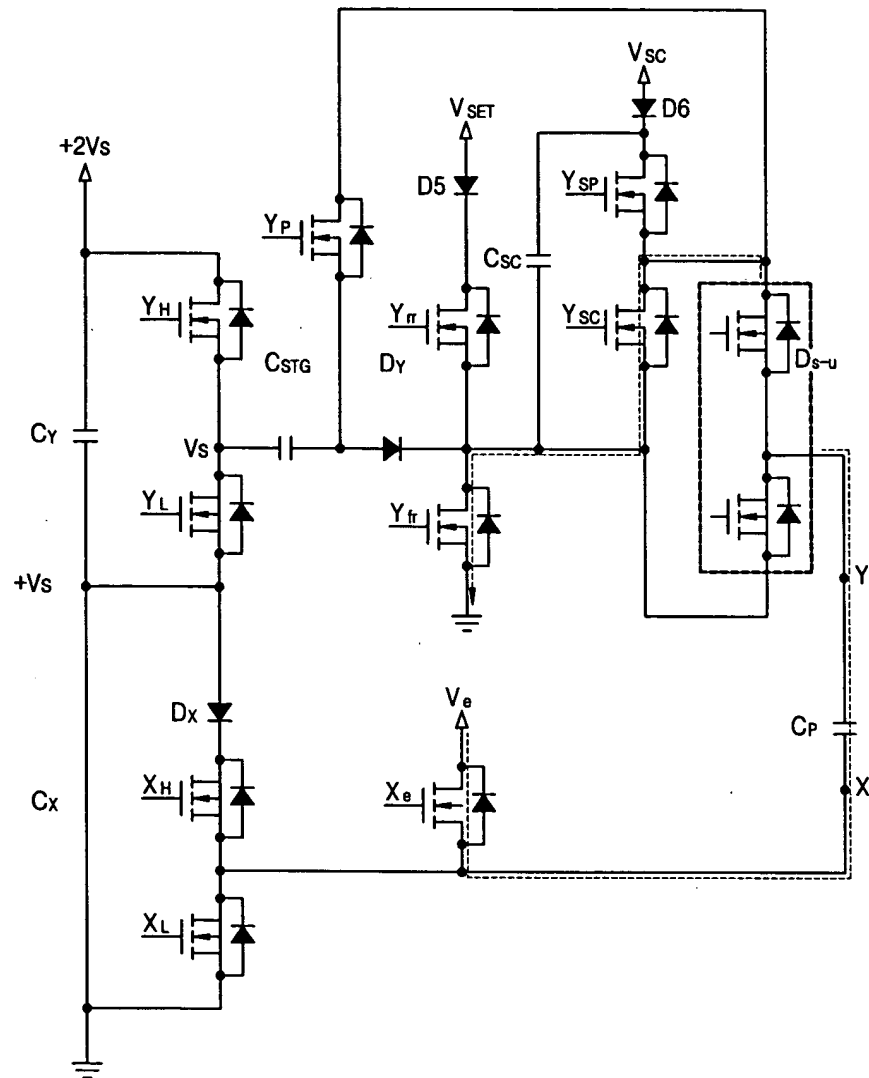


FIG. 8

